

Paper Publication: 2017-18

Sr. No	Name of Faculty	Title of Paper	Journal Details	Type of Publication (IJ/NJ), (peer-reviewed, Google scholar, Scopus indexed)	Date and Year of Publication	Name of Authors	DOI No	Volume & Pages	ISBN/ISSN No.
1	Dr. P. Malathi	Employability Skill Matrix for Engineering Graduates of Tier-II Institutes	Journal of Engineering Education Transformations	IJ	2017.	VA Kulkarni, AK Bewoor, P Malathi, BS Balapgol	10.16920/jeet/2017/v30i3/110512	(3), 71-76	
2	Mrs. S.N.Mane	Study of Routing Overhead and Its Protocols	Journal of Advanced Engineering and Management	IJ	2017	N Lavande, S Shilgire	-	2 (2), 52-55	
3	KB Bhangale	Patterned Fabric Defect Detection Using Wavelet Golden Image Subtraction Method SG Naik, MS Biradar, KB Bhangale	International Journal of Advance Research, Ideas and Innovations in Technology.	IJ	2017	SG Naik, MS Biradar,	-	(Volume3, Issue3)767-771	ISSN: 2454-132X
4	Dr. Manish Sharma	Low Power , Area Efficient Dynamic Voltage Comparator with Reduced Activity Factor	Journal of communications technology, Electronics and Computer Science ,	IJ	2017	Amol Shinde		Vol.5, Issue 12, pp.20-25	ISSN 2457-905x
5	Mrs. Trupti Wagh	Dynamic Spectrum Allocation for Network Utility and Through Maximization in Hybrid Access Cognitive Femtocell network	International Journal of Innovative Research in Computer and Communication Engineering	IJ	June 2017	Sanjyot Godse	10.15680/IJRCCE.2017.0506001	Vol. 5, Issue 6, pp.1111-1117	ISSN: 2320-9801
6	Mrs. Usha Jadhav	Study and Analysis	International Journal for	IJ	May 2017	Poonam S.Wankhede		Vol. 3, Issue 5,	ISSN (Online)

		Probability of Soft Error in Combinational Circuits Using NOR Gate	Science and Advance Research in Technology (IJSART)					pp.358-361	2395-1052
7	Mrs. Usha Jadhav	Design and Analysis of NAND Gate using 180nm and 90nm CMOS Technology	International Journal for Science and Advance Research in Technology (IJSART)	IJ	May 2017	Poonam S.Wankhede		Vol. 3, Issue 5, pp.362-365	ISSN (Online) 2395-1052
8	Mrs. Usha Jadhav	Design and implementation of Fir Filter Structure Using High Adders and Wallace Tree Multiplier	International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering	IJ	April 2017	Ms.K Priyanka Sahu	10.15662/IJA REEIE .2017. 06040 01	Vol. 5, Issue 6, pp.1-6	ISSN (Online) 2278-8875
9	Mrs. Usha Jadhav	Improved FIR Filter Design Using Wallace Tree Compression	International Journal of Innovative Research in Computer and Communication Engineering	IJ	March 2017	Ms.K Priyanka Sahu	10.15680/IJI RCCR E.2017 .05030 01	Vol. 4, Issue 6, pp.1111-1115	